

ABSTRACT OF THE DISCLOSURE

A device for seating a semiconductor device in a semiconductor test handler is provided. The device includes a plate having a plurality of device seating members each for seating the semiconductor device. The device also includes a latch rotatably mounted to one side of the device seating member for pressing down or freeing the semiconductor device seated on the device seating member. A latch operating means for causing the latch to press down the semiconductor device when the semiconductor device is seated on the device seating member is also included. The provided device also releases the pressing down action when the semiconductor device is seated on the device seating member and when the semiconductor device is taken away from the device seating member, thereby seating the semiconductor device on the device seating device accurately and positively.